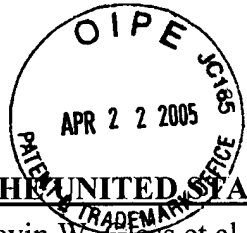


DPW



S/N 10/727,262

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Kevin W. Glass et al.	Examiner:	Khanh V. Nguyen
Serial No.:	10/727,262	Group Art Unit:	2817
Filed:	December 3, 2003	Docket No.:	80107.099US1
Title:	BIASED DARLINGTON TRANSISTOR PAIR, METHOD, AND SYSTEM		
Assignee:	Intel Corporation		

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicant has reviewed the Office Action mailed on February 22, 2005. Please amend the above-identified patent application as follows.